

FIG. 1 PRIOR ART

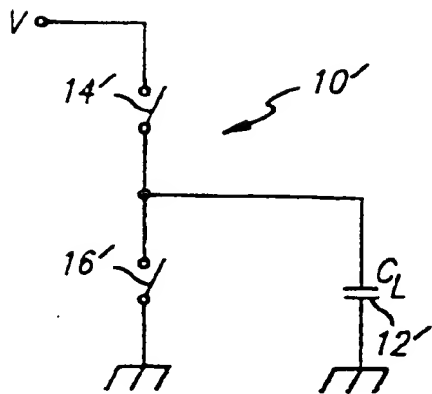


FIG. 2 PRIOR ART

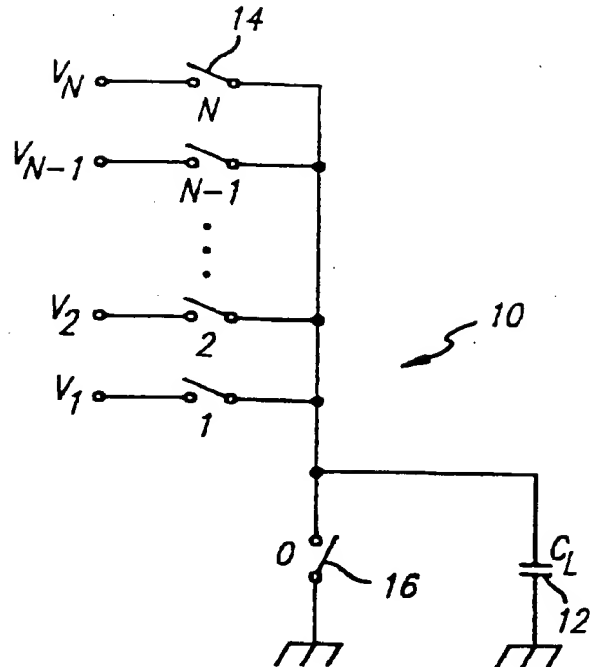


FIG. 3

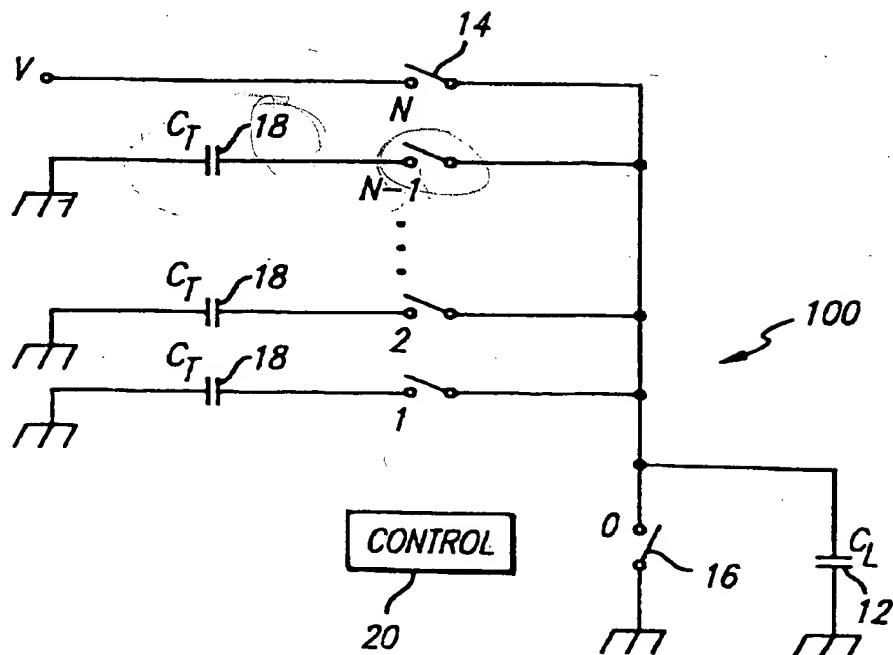
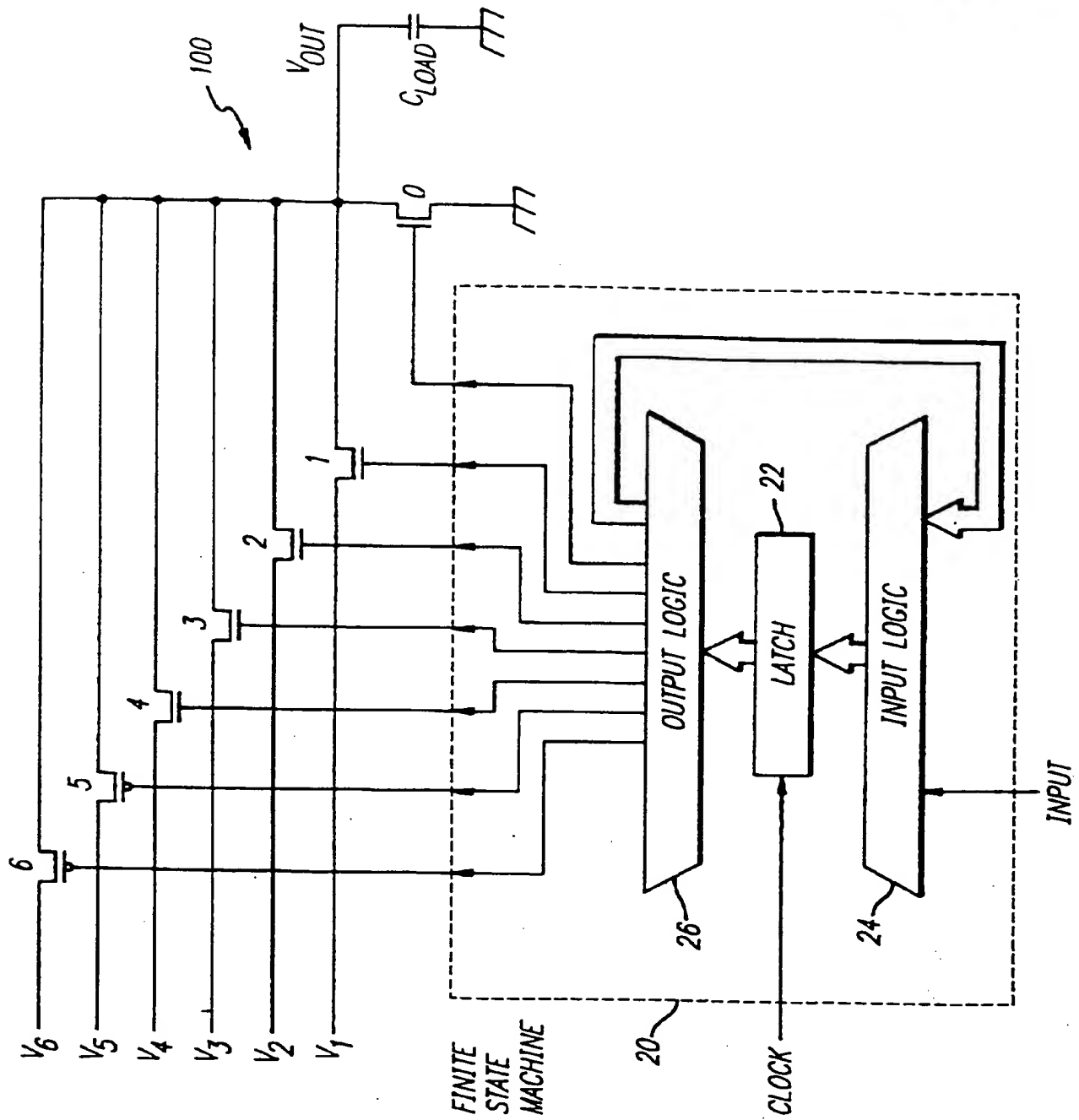
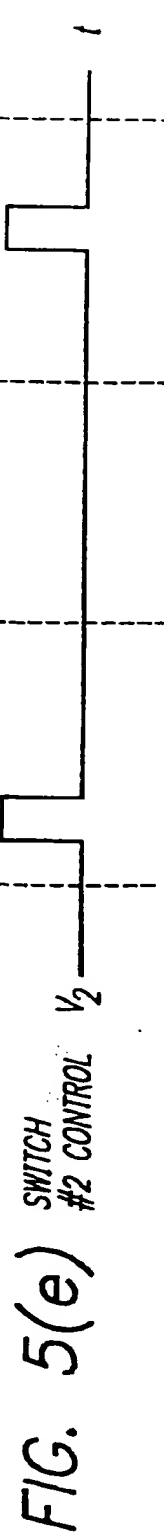
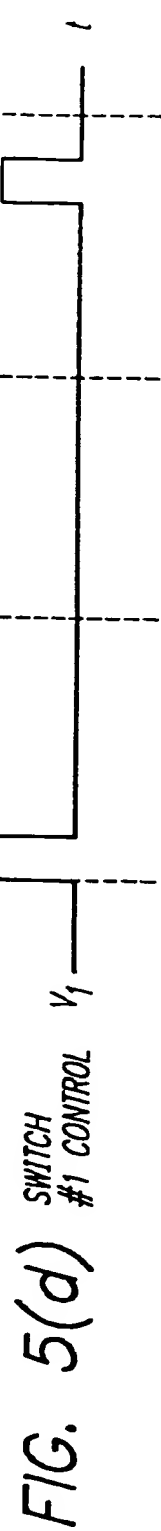
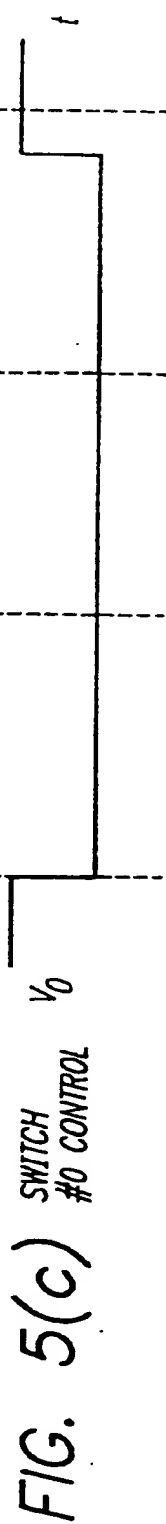


FIG. 4





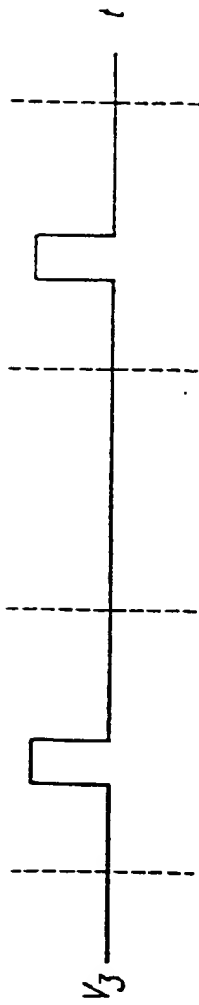


FIG. 5(f) SWITCH #3 CONTROL  $V_3$

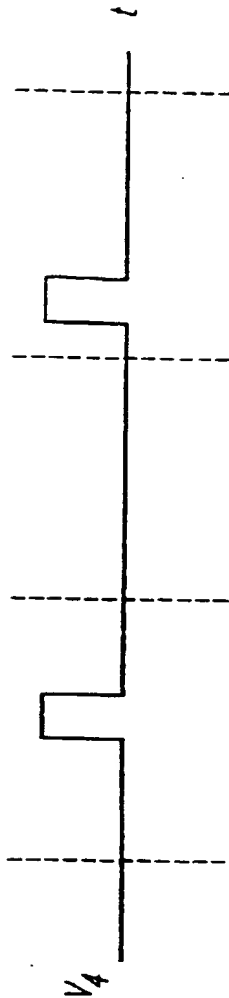


FIG. 5(g) SWITCH #4 CONTROL  $V_4$

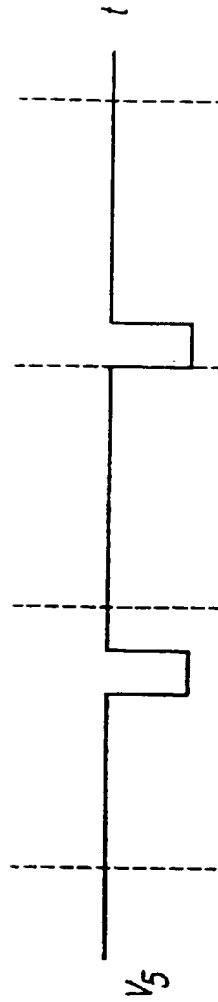


FIG. 5(h) SWITCH #5 CONTROL  $V_5$

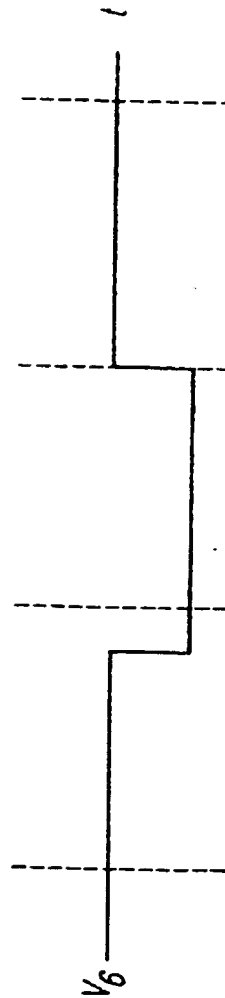


FIG. 5(i) SWITCH #6 CONTROL  $V_6$

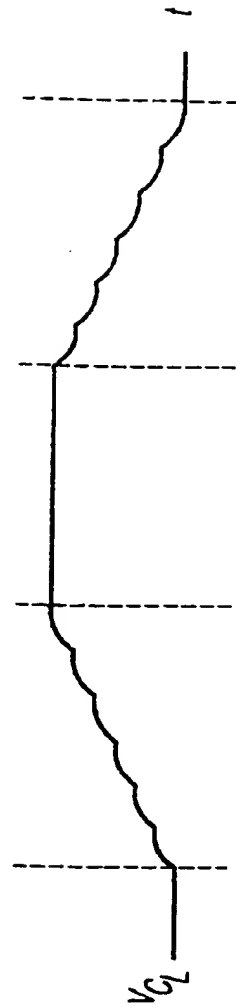


FIG. 5(j) OUTPUT  $V_{CL}$